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PTO/SB/08A (10-99)

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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	09/749,286
		Filing Date	December 26, 2000
		First Named Inventor	Gene R. Anderson
		Group Art Unit	2878
(use as many sheets as necessary)		Examiner Name	Pyo, Kevin K.
Sheet	1	of	1
		Attorney Docket Number	1613370-0009

U.S. PATENT DOCUMENTS							
Examiner Initials *	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number-Kind Code ² (if known)					
PKK		US-5,226,052		07-06-1993	Tanaka et al.		
PKH		US-5,907,571		05-25-1999	Ogino et al.		
FOREIGN PATENT DOCUMENTS							
Examiner Initials *	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ₃
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
PKK		WO	97 25638 A3	07-17-1997	Siemens		
		WO	97 25641 A3	07-17-1997	Siemens AG		
		EP	0 790 678 A3	08-20-1997	Alcatel		
		JP	10 126002	08-31-1998	Matsushita		
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		EP	0 607 700 B1	08-25-1999	Sharp		
		WO	00 13051 A1	03-09-2000	Digital Optics		
		EP	0 974 856 A2, A3	11-02-2000	Honeywell		
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U.S. PATENT DOCUMENTS							
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		Number-Kind Code ² (if known)					
PKK ↑ ↓		US - 4,563,725		01-07-1986	Kirby		
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Examiner Initials *	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
PKK ↑ ↓		JP	56 111822		09-03-1981	Oki Densen	
		EP	0 440 336 A2		08-07-1991	Amphenol Corp.	
		DE	41 23 370 A1		01-21-1993	Robock	
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		EP	1 028 341 A2		08-16-2000	Hewlett-Packard	
		WO	00 73833 A1		12-07-2000	E20 Commun.	
OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS							
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PKK ↑ ↓		ROBINSON, et al., Low Cost Molded Packaging for Optical Data Links, 1994 IEEE Journal, pp. 312-316					
		KIBAR et al., Power Minimization and Technology Comparisons for Digital Free-Space Optoelectronic Interconnections, 1999 Journal of Lightwave Technology, Vol. 17, No. 4, pp. 546-555					
		SIMONIS et al., 1 Gb/s VCSEL/CMOS Flip-Chip 2-D Array Interconnects and Associated Diffactive Optics, 1999 Parallel Interconnect (PI '99), pp. 1-9					
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